



FORM PTO-1449

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SP038.C5 (1397.0140005)

APPLICATION NO.  
10/815,742

FIRST NAMED INVENTOR: WANG et al.

First Supplemental  
INFORMATION DISCLOSURE STATEMENT

FILING DATE  
April 2, 2004

ART UNIT  
2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA6					
	AB6					
	AC6					
	AD6					
	AE6					
	AF6					
	AG6					
	AH6					
	AI6					
	AJ6					
AK6						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
RUE	AL3	61-107434	05/26/1986	Japan		X Yes
RUE	AM3	3-218524	09/26/1991	Japan		X Yes
RUE	AN3	0 354 585	02/14/1990	EPO		Yes
	AO3					Yes No
	AP3					Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	11	HARA et al., "Branch Pipeline of DSNS Superscalar Processor Prototype", <i>Computer Architecture</i> , 86-3, January 24, 1991, pp 1-8 in Japanese language (with English language translation, 20 pages).
	AS	11	
	AT	11	

EXAMINER Richard Ellis

DATE CONSIDERED 3/22/2005

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.